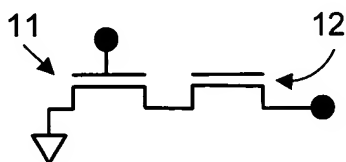


PRIOR ART
Figure 1



PRIOR ART
Figure 1A

SG EE	Erase	Program	Read
SG Wordline (WL)	-17 V (-12 V)	V _t	V _{ref}
Bitline (BL)	V _{ss} (5V)	12 V	V _{dd}
Source Line (SR)	V _{ss}	V _{ss}	V _{ss}

PRIOR ART
Figure 1B